

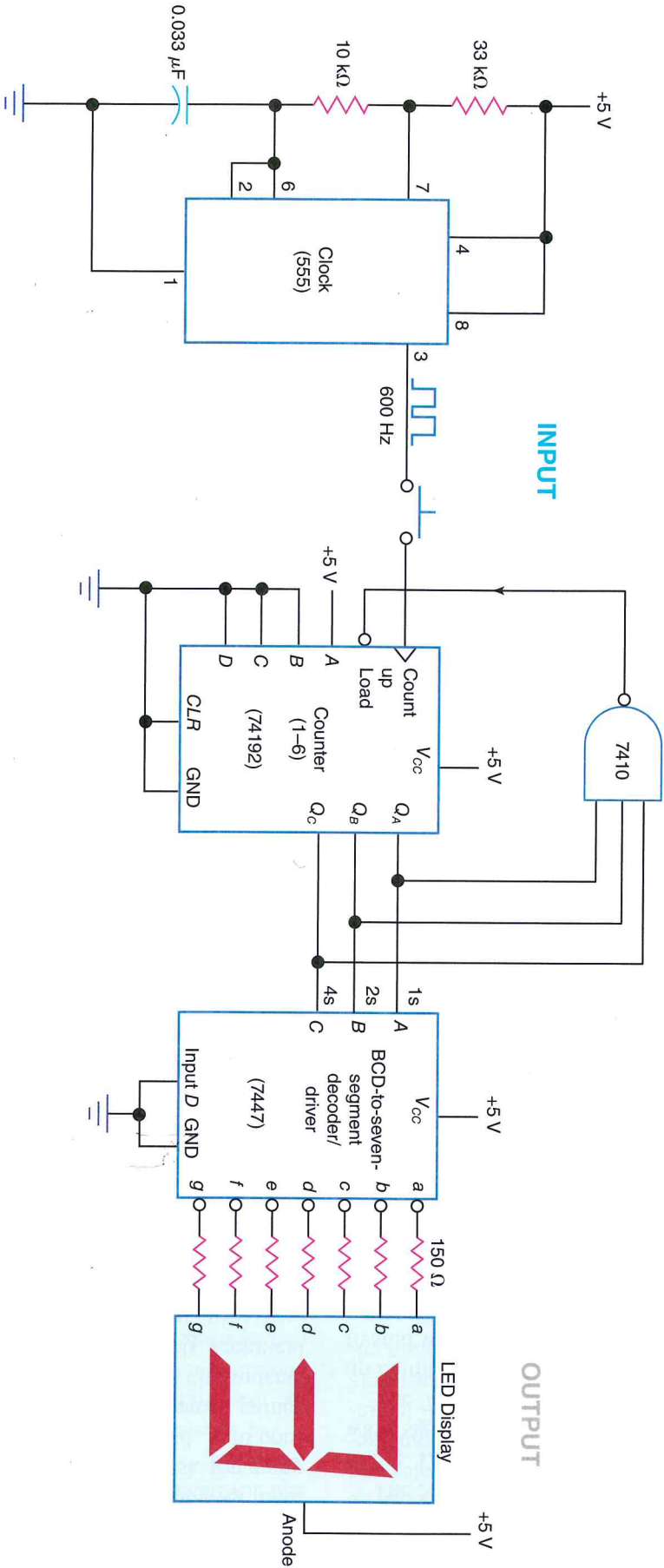


Fig. 12-4 Wiring diagram for an experimental digital dice game using TTL ICs.