

4. Write the SOP expression derived from the truth table. Use Boolean algebra to simplify the expression as much as possible.

5. Draw the circuit described by the simplified Boolean expression in Step 4. Connect the circuit using only 74LS10 or 74HC10 triple 3-input NAND gates and 74LS04 or 74HC04 inverters. Refer to Figures 6.2 and 6.3 for guidance.

6. Take the truth table of the circuit drawn in Step 5. Verify that it is the same as the table constructed in Step 3.

Instructor's Initials _____

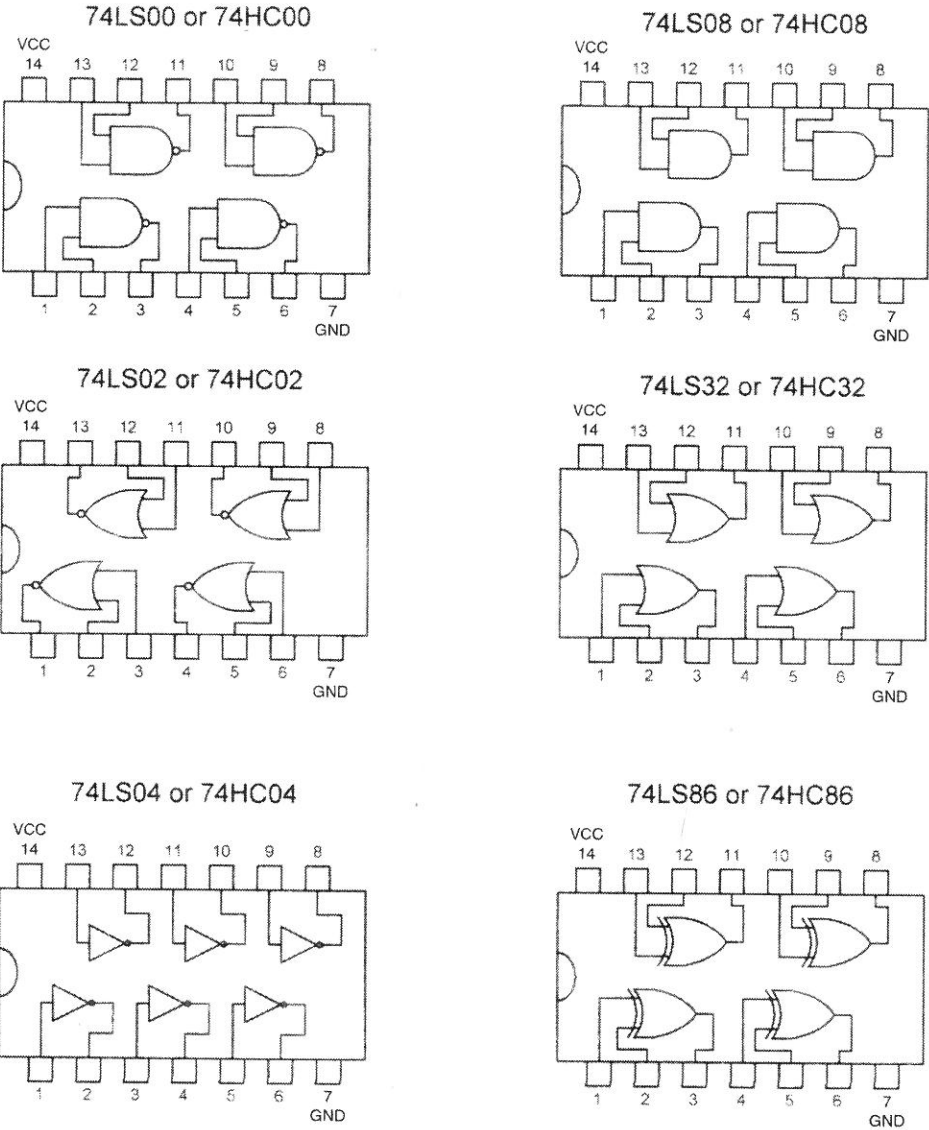


Figure 6.3 Pinouts of DIP ICs