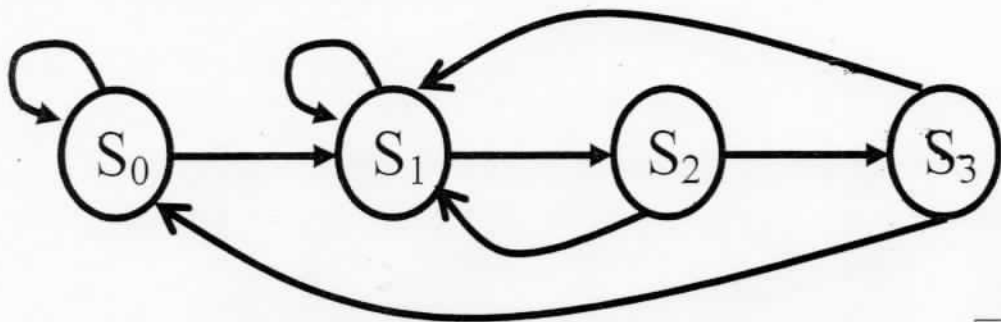


1. Design Mealy sequential circuit with 1 input X and 2 outputs Y and Z. Y=1 when the sequence 011 is completed and Z=1 when 0111 is completed. Derive the state graph. Use the template provided adding needed legs, Label legs with format X/YZ {2.2}



State	Meaning
S ₀	
S ₁	
S ₂	
S ₃	